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Sheet 1 of 1

Form 1449*

INFORMATION DISCLOSURE STATEMENT
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Applicant: Leonard Forbes et al.

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U.S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
MAT	6,166,401	12/26/2000	Forbes, L.	257	77	08/20/98

FOREIGN PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No

OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

MAT	Wolf, "Silicon Processing for the VLSI Era Vol. 2: Process Integration", Lattice Press, 623-626, (1990)
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Examiner Michael Trinh Date Considered 3/04/02

*Substitute Disclosure Statement Form (PTO-1449)

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